

932
10/002925
11/02/01

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10002925	11/02/2001	257		2813	BEREZNY, N

**APPLICANTS: Kux Andreas; Smola Michael;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CON OF PCT/EP00/03834 04/27/2000

** FOREIGN APPLICATIONS VERIFIED:

EUROPEAN PATENT OFFICE (EPO) 99 108 768.5 05/03/1999

PG-PUB ☐ DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☐ yes ☐ no

35 USC 119 conditions met

☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

1999P1778

TITLE : Method and device for securing a multi-dimensionally constructed chip stack and chip configuration

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)